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<u>L17</u>	L14 and l7	1	<u>L17</u>
<u>L16</u>	L14 and l6	0	<u>L16</u>
<u>L15</u>	L14 and l5	2	<u>L15</u>
<u>L14</u>	l4 and processing near3 (cell\$1 or element\$1 or block\$1)	83	<u>L14</u>

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<u>L10</u>	l4 and l5	4	<u>L10</u>
<u>L9</u>	(716/13,14,17)[CCLS]	1064	<u>L9</u>
<u>L8</u>	(716/2-21)[CCLS]	7613	<u>L8</u>
<u>L7</u>	(716/2-21)![CCLS]	7613	<u>L7</u>
<u>L6</u>	(712/15,17,18,35,36,37,38)![CCLS]	1016	<u>L6</u>
<u>L5</u>	(712/2-300)[CCLS]	10878	<u>L5</u>

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<u>L3</u>	L1 and (cell\$1 or block\$1 or macro)	865	<u>L3</u>
<u>L2</u>	L1 near35 (cell\$1 or block\$1)	11	<u>L2</u>
<u>L1</u>	micro near1 controller\$1 near35 program\$6 near25 (analog\$5 or digital)	957	<u>L1</u>

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